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| **Date:** | **28 MAY 2020** | **Name:** | **K Gaurav Shet** |
| **Course:** | **logic design** | **USN:** | **4AL18EC023** |
| **Topic:** | **Day 2:analysis of clocked sequential circuits**  **digital clock design** | **Semester & Section:** | **4TH SEM**  **& A SEC** |
| **Github Repository:** | **Gaurav-shet** |  |  |

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| **FORENOON SESSION DETAILS** |
| **Image of session** |



